

AMENDMENTS TO THE ABSTRACT

Please add the following text beginning on page 10, line 1 of the application.

--ABSTRACT

The invention concerns a ROM circuit (40) comprising columns of storage cells, each column being connected to a bit site (BL_i , BL_{i+1}), wherein the columns are arranged in groups of two adjacent columns, each column of a group capable of being selectively activated relative to the other column of the group, thereby enabling the elimination of a connection to the ground of columns and the design of efficient reading amplifiers.--